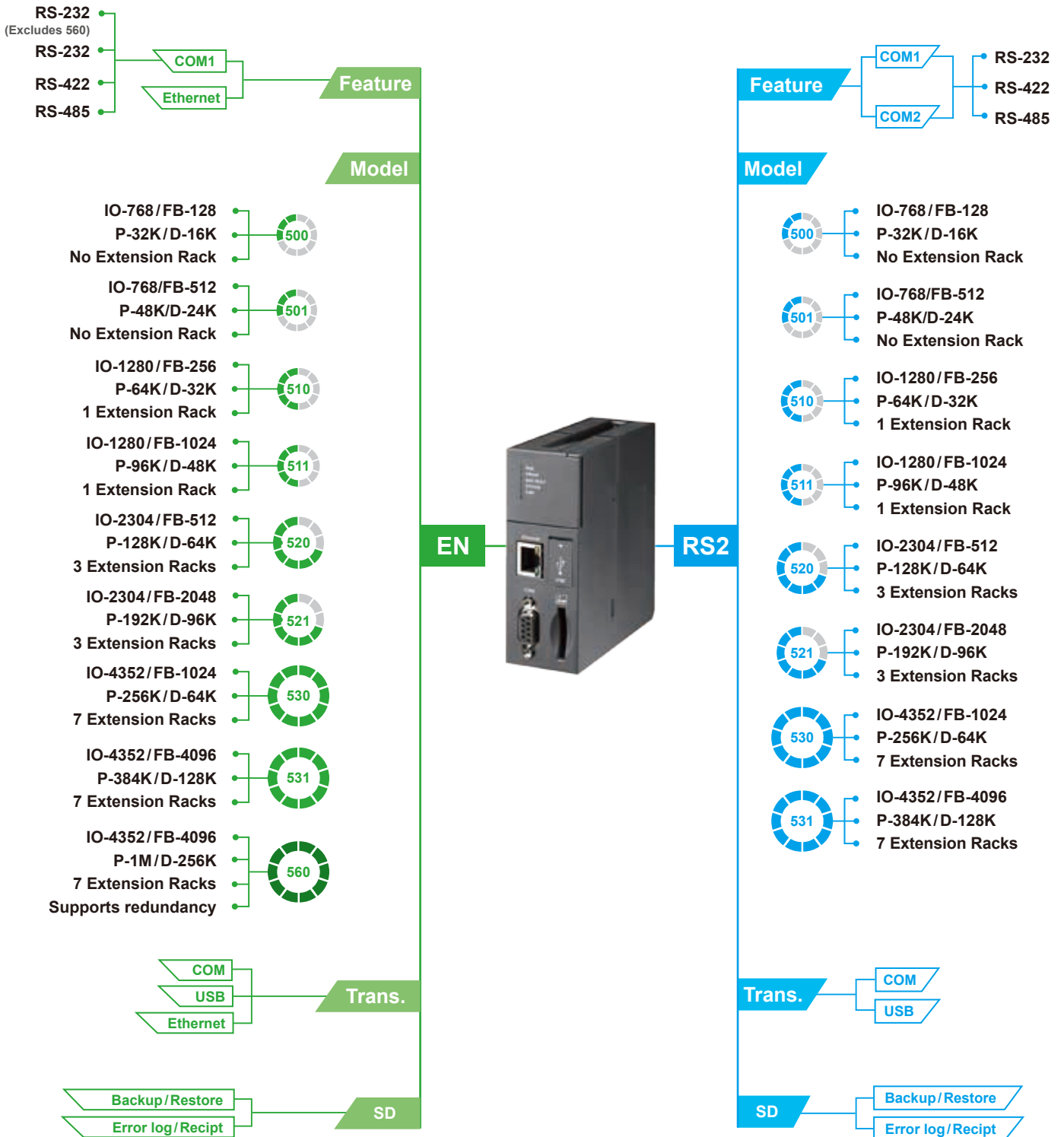


Specification Tree



CPU Selection Table

Item	Specifications	Check	CPU Model																	
			AH500		AH510		AH520		AH530		AH501		AH511		AH521			AH531		AH560
			RS2	EN	RS2	EN	RS2	EN	RS2	EN	RS2	EN	RS2	EN	RS2	EN	DNP	RS2	EN	EN2
Local I/O points	< 768	<input type="checkbox"/>	•							•										
	< 1280	<input type="checkbox"/>		•							•									
	< 2304	<input type="checkbox"/>			•								•							
	< 4352	<input type="checkbox"/>					•									•		•		
Program capacity	< 32k steps	<input type="checkbox"/>	•								•									
	< 48k steps	<input type="checkbox"/>								•										
	< 64k steps	<input type="checkbox"/>		•																
	< 96k steps	<input type="checkbox"/>										•								
	< 128k steps	<input type="checkbox"/>				•														
	< 192k steps	<input type="checkbox"/>												•						
	< 256k steps	<input type="checkbox"/>							•											
	< 384k steps	<input type="checkbox"/>															•			
< 1M steps	<input type="checkbox"/>																	•		
Expansion capacity	None	<input type="checkbox"/>	•							•										
	< 1 expansion rack	<input type="checkbox"/>		•								•								
	< 3 expansion racks	<input type="checkbox"/>				•								•						
	< 7 expansion racks	<input type="checkbox"/>							•								•		•	
Built-in communication	1 COM port	<input type="checkbox"/>		•		•		•		•		•		•		•		•	•	
	2 COM ports	<input type="checkbox"/>	•		•		•		•		•		•		•		•			
	Ethernet	<input type="checkbox"/>		•		•		•		•		•		•		•		•	•	
	Mini-USB	<input type="checkbox"/>	•		•		•		•		•		•		•		•		•	
Protocol supported	Modbus	<input type="checkbox"/>	•		•		•		•		•		•		•		•		•	
	Modbus TCP	<input type="checkbox"/>		•		•		•		•		•		•		•		•	•	
	EtherNet/IP	<input type="checkbox"/>								•		•		•		•		•	•	
	DNP3	<input type="checkbox"/>													•					
SD card	V 1.0	<input type="checkbox"/>	•		•		•		•											
	V 2.0 (SDHC)	<input type="checkbox"/>								•		•		•		•				
	V 2.0 (Micro SDHC)	<input type="checkbox"/>																	•	
Redundancy	CPU	<input type="checkbox"/>																	•	

Model Name

AH CPU

AHCPU500-RS2

AH	CPU	5	0	0	-	RS2
Series	Classification	Model	Function	Version		Type
	CPU		0: No expansion rack 1: 1 expansion rack 2: 3 expansion racks 3: 7 expansion racks 6: Redundant CPU			RS2: 2 COM ports EN: 1 COM & 1 Ethernet ports EN2: 1 COM & 2 Ethernet ports DNP: 1 COM & 1 Ethernet ports, supports DNP3

AH Power Supply Module

AHPS05-5A

AH	PS	05	-	5A
Series	Classification	Function		Type
	Power supply	05: AC input (100~240V) 15: DC input (24V)		

AH RTU Module

AHRTU-DNET-5A

AH	RTU	-	DNET	-	5A
Series	Classification		Function		Type
	Remote terminal unit		DNET: DeviceNet PFBS: PROFIBUS ETHN: EtherNet/IP		

AH Digital I/O Module

AH16AM10N-5A

AH	16	AM	1	0	N	-	5A
Series	I/O points	Classification	Function	Function	Function		Type
	16: 16 points	AM: Digital input	0: No input	0: No output	N: No output		5A: Removable terminal
	32: 32 points	AN: Digital output	1: DC input (24V)	1: 0.5A transistor/TRIAC output or 2A relay output	R: Relay output		5B: DB37 connector
	64: 64 points	AP: Digital input/output	3: AC input (120~240V)	2: 0.1A transistor output	T: NPN output		5C: MIL
		AR: Digital input with interrupt			P: PNP output		
					S: TRIAC output		

AH Analog I/O Module

AH04AD-5A

AH	04	AD	-	5A
Series	I/O Channels	Classification		Type
	04: 4-channel 06: 6-channel 08: 8-channel	AD: Analog input DA: Analog output XA: Analog input/output		5A: Voltage/Current 5B: Voltage 5C: Current

AH Backplane

AHBP04M1-5A

AH	BP	04		M1	-	5A
Series	Classification	Function		Function		Type
	Backplane	00: No slot 03: 3-slot 04: 4-slot 05: 5-slot	06: 6-slot 07: 7-slot 08: 8-slot 12: 12-slot	M1: Main backplane M2: Main backplane for motion E1: Expansion backplane MR1: Redundant main backplane ER1: Redundant expansion backplane		

AH Network Module

AH10EN-5A

AH	10	EN		-	5A
Series	Function	Classification			Type
	10: Basic 15: Advanced	EN: Ethernet SCM: Serial communication DNET: DeviceNet	PFBM: PROFIBUS master PFBS: PROFIBUS slave COPM: CANopen		

AH Motion Module

AH02HC-5A

AH	02		HC	-	5A
Series	Function		Classification		Type
	02: 2-channel 04: 4-channel 05: Simple type (PM) 10: Standard type (PM) 15: Advanced type (PM)	20: DMCNET 08E: EtherCAT (8-axis) 10E: EtherCAT (16-axis) 20E: EtherCAT (32-axis)	HC: High speed counter PM: Motion controller (Pulse train) MC: Motion controller (Network)		


AH Temperature Module

AH04PT-5A

AH	04	PT	-	5A
Series	I/O channels	Classification		Type
	04: 4-channel 08: 8-channel	PT: Platinum resistance thermometer TC: Thermocouple PTG: Platinum resistance thermometer (channel isolation)		

Ordering Information


CPU Modules

Model	Local I/O points	Program capacity	Data register D/L/B (note)	Function blocks	Extension backplane	LD execution speed (μs)	Power consumption (Internal)	Specifications	Certificates
AHCPU 500-RS2	768	32k steps (128 KB)	16k/16k/512k words	128	0	0.1	2w	<ul style="list-style-type: none"> ▪ Built-in RS-232/422/485 multi-modes communication portx2 (RS-232: 115.2 kbps/ RS-422/485: 921.6 kbps) ▪ Built-in SD card slot (supports max. 2 GB for AHCPU5_0 or 32 GB for AHCPU5_1) ▪ Built-in Mini-USB programming port ▪ System diagnosis/ status light / online editing and debug functions ▪ PLC Link automatic data exchange function ▪ Modbus RTU/ASCII ▪ LD/SFC/FBD/IL/ST languages ▪ 256 interrupts (Timed/IO/External/ Low voltage/Communication) ▪ 2,048 timers and counters ▪ No battery required ▪ RTC function (max. 30 days after power off) ▪ CPU RAM/ROM capacity AHCPU5_0: 32MB/8MB AHCPU5_1: 128MB/128MB ▪ CPU clock AHCPU5_0: 133 MHz AHCPU5_1: 800 MHz 	
AHCPU 501-RS2	768	48k steps (196 KB)	24k/24k/512k words	512	0	0.02	2.9w		
AHCPU 510-RS2	1,280	64k steps (256 KB)	32k/32k/1,024k words	256	1	0.1	2w		
AHCPU 511-RS2	1,280	96k steps (384 KB)	48k/48k/1,024k words	1,024	1	0.02	2.9w		
AHCPU 520-RS2	2,304	128k steps (512 KB)	64k/64k/2,048k words	512	3	0.1	2w		
AHCPU 521-RS2	2,304	192k steps (768 KB)	96k/96k/2,048k words	2,048	3	0.02	2.9w		
AHCPU 530-RS2	4,352	256k steps (1 MB)	64k/64k/4,096k words	1,024	7	0.1	2w		
AHCPU 531-RS2	4,352	384k steps (1.5 MB)	128k/128k/4,096k words	4,096	7	0.02	2.9w		

Note: Data Register B is for the use of function blocks.

Accessory Selection for High-density Modules

CPU Modules

Model	Local I/O points	Program capacity	Data register D/L/B (note)	Function blocks	Extension backplane	LD execution speed (µs)	Power consumption (Internal)	Specifications	Certificates
AHCPU 500-EN	768	32k steps (128KB)	16k/16k/512k words	128	0	0.1	2w	<ul style="list-style-type: none"> ▪ Built-in RS-232/422/485 multi-modes communication port x1 (RS-232:115.2 kbps/ RS-422/485: 921.6 kbps) ▪ Built-in Ethernet communication port (100 Mbps) ▪ Built-in SD card slot (supports max. 2 GB for AHCPU5_0 or 32 GB for AHCPU5_1) ▪ Built-in Mini-USB programming port ▪ System diagnose/ status light/online editing and debug functions ▪ PLC Link automatic data exchange function ▪ Modbus RTU/ASCII, Modbus TCP ▪ DNP3 (AHCPU521-DNP only) ▪ EtherNet/IP (AHCPU5_1-EN only) <ul style="list-style-type: none"> - Scanner & Adapter mode - Supports I/O connection & explicit message - Connections: TCP= 32 ~ 128; CIP= 64 ~ 256 - RPI: 1 ~ 1,000ms - 250 words / connection ▪ LD/SFC/FBD/IL/ST languages ▪ 256 interrupts (Timed/IO/External/ Low voltage/Communication) ▪ 2,048 timers and counters ▪ No battery required ▪ RTC function (max. 30 days after power off) ▪ NTP network time correction function ▪ WEB/E-mail/IP Filter function ▪ CPU RAM/ROM capacity AHCPU5_0: 32 MB/8 MB AHCPU5_1: 128 MB/128 MB ▪ CPU clock AHCPU5_0: 133 MHz AHCPU5_1: 800 MHz 	
AHCPU 501-EN	768	48k steps (196 KB)	24k/24k/512k words	512	0	0.02	2.9w		
AHCPU 510-EN	1,280	64k steps (256KB)	32k/32k/1,024k words	256	1	0.1	2w		
AHCPU 511-EN	1,280	96k steps (384 KB)	48k/48k/1,024k words	1,024	1	0.02	2.9w		
AHCPU 520-EN	2,304	128k steps (512KB)	64k/64k/2,048k words	512	3	0.1	2w		
New AHCPU 521-EN/DNP (note 2)	2,304	192k steps (768 KB)	96k/96k/2,048k words	2,048	3	0.02	2.9w		
AHCPU 530-EN	4,352	256k steps (1MB)	64k/64k/4,096k words	1,024	7	0.1	2w		
AHCPU 531-EN	4,352	384k steps (1.5MB)	128k/128k/4,096k words	4096,	7	0.02	2.9w		

Note 1 : Data Register B is for the use of function blocks.

Note 2 : Please contact our distributors for release date.